

Title: FREEWAY GATE ARRAY ARCHITECTURE

Filed: February 15, 2002

Inventors: Liu, et al.

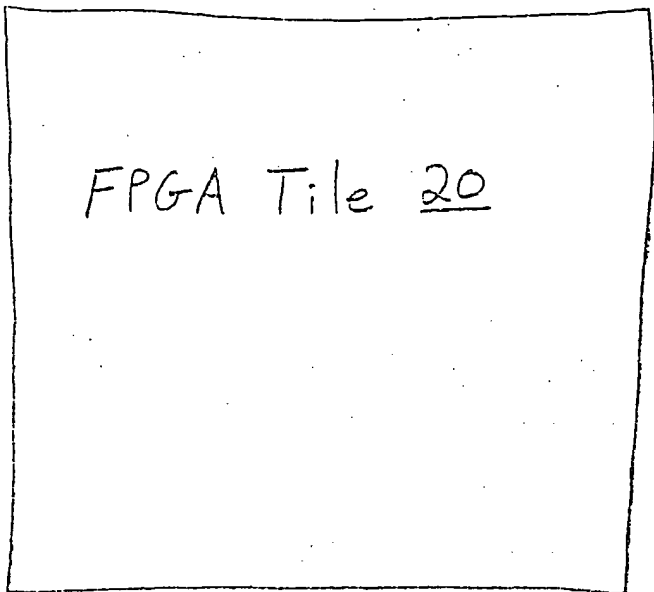
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Attorney: Michael A. Blake

Sierra Patent Group, Ltd.



FPGA Tile 20

FIG. 1

10077189.021502

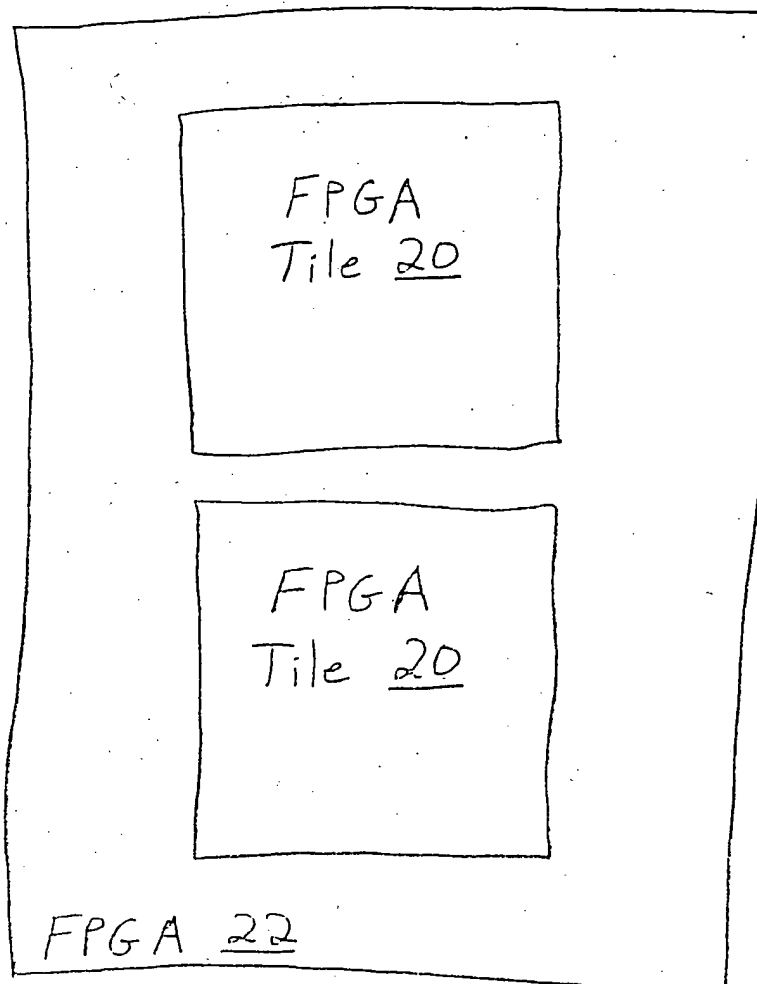


FIG. 2

205T20-68T200T

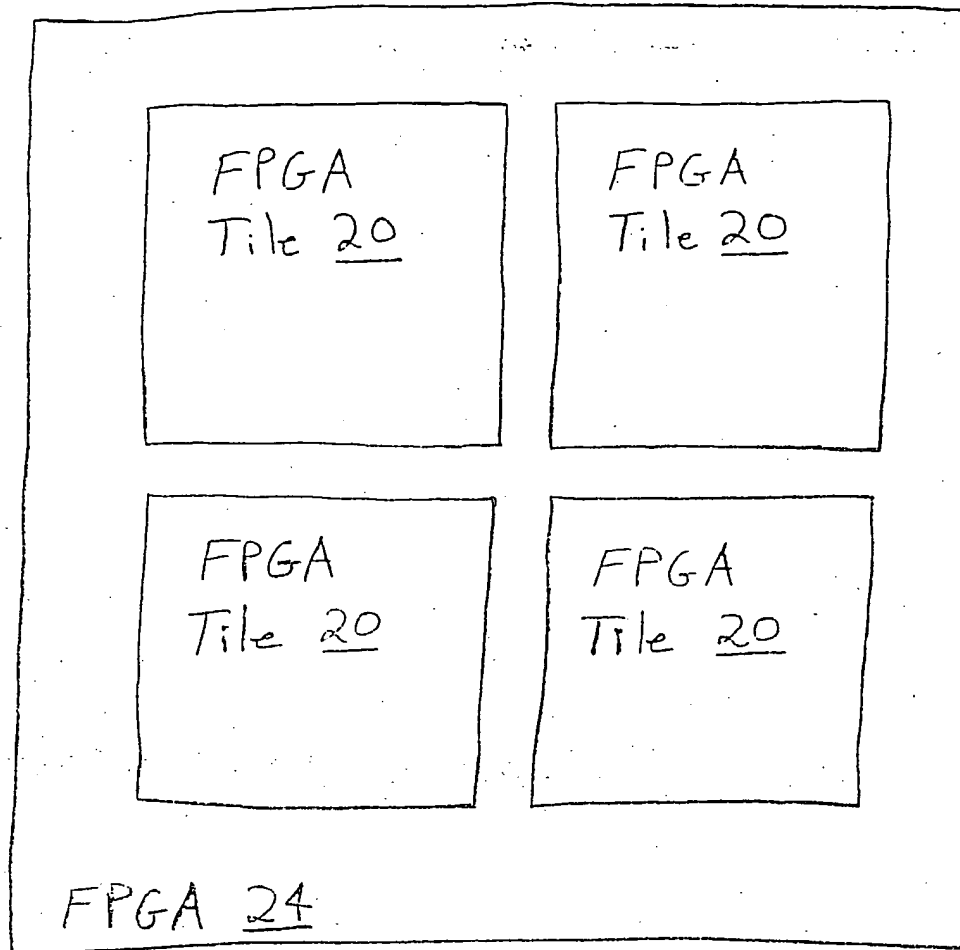


FIG. 3A

205T20" 68T400T

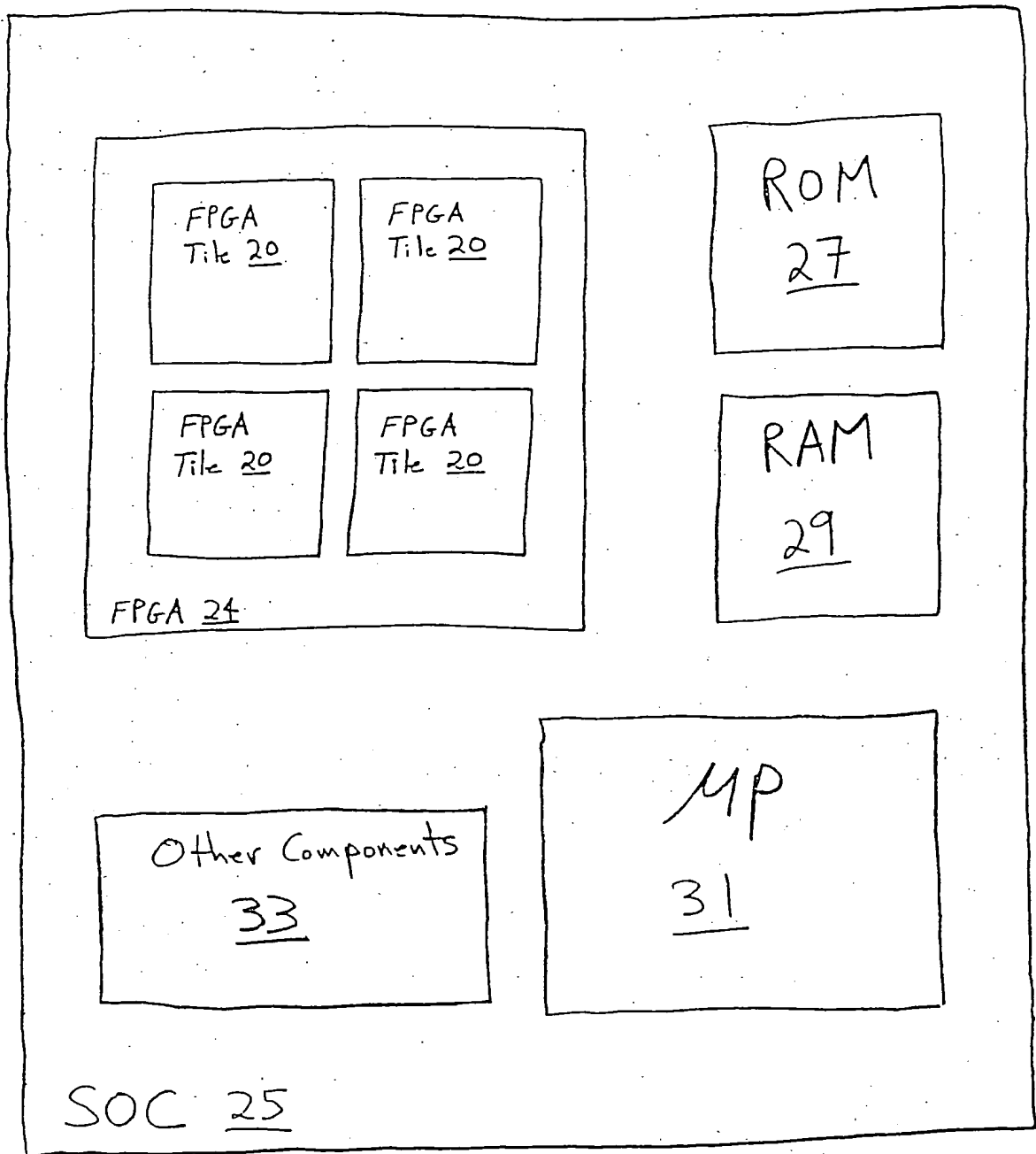


FIG. 3B

2007189-021500

205T20" 6BT2200T

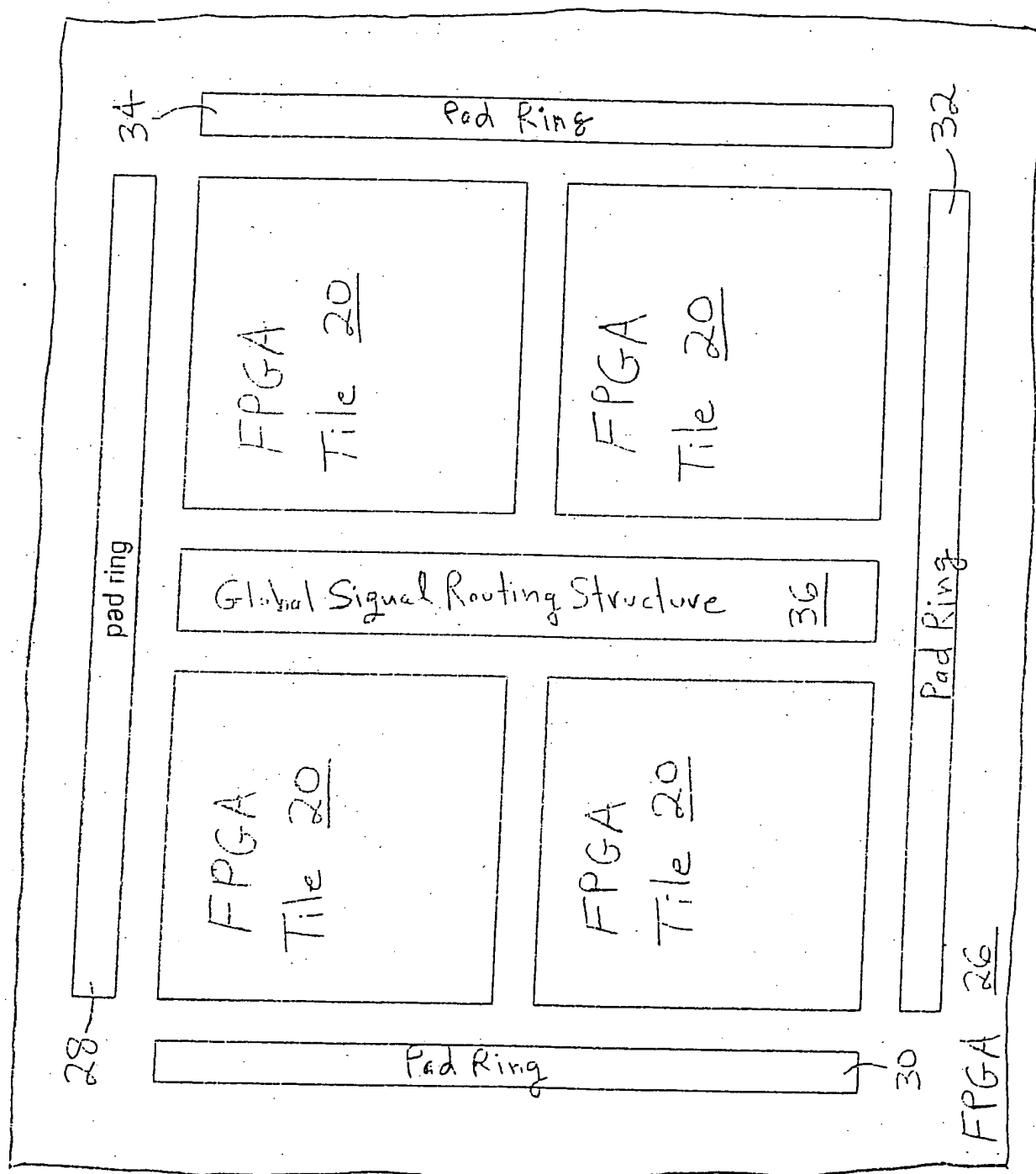


FIG 4

2057139-021502

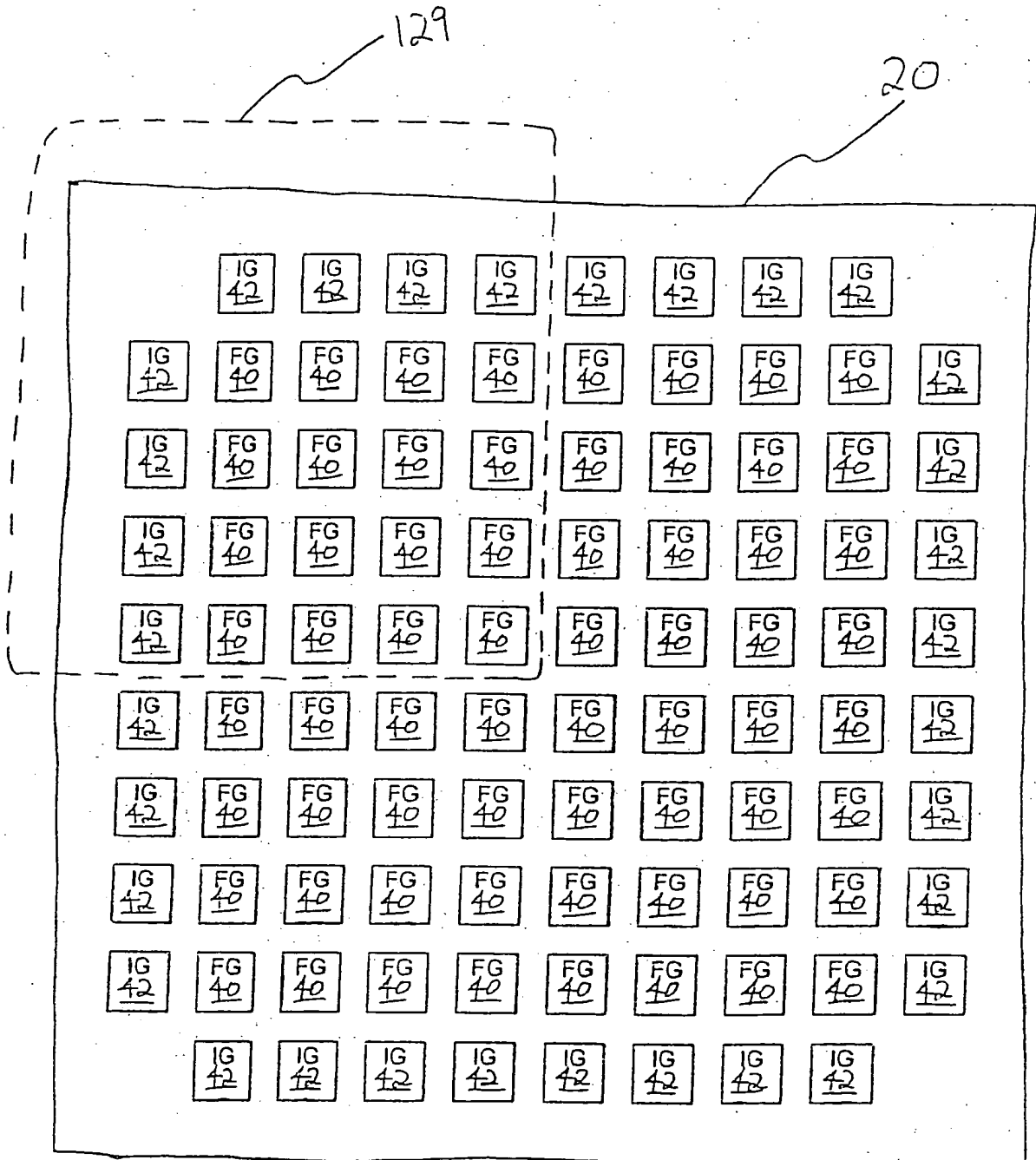


FIG. 5

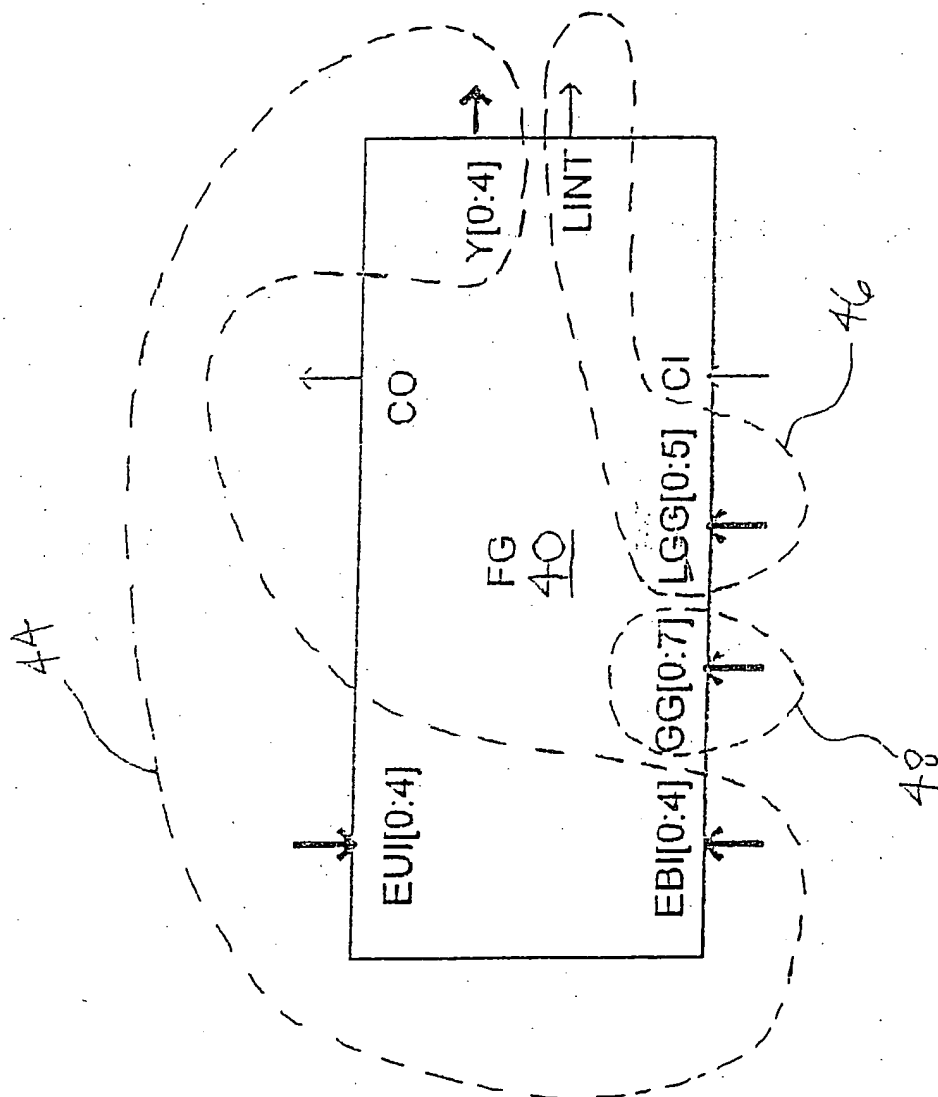


FIG. 6

7614



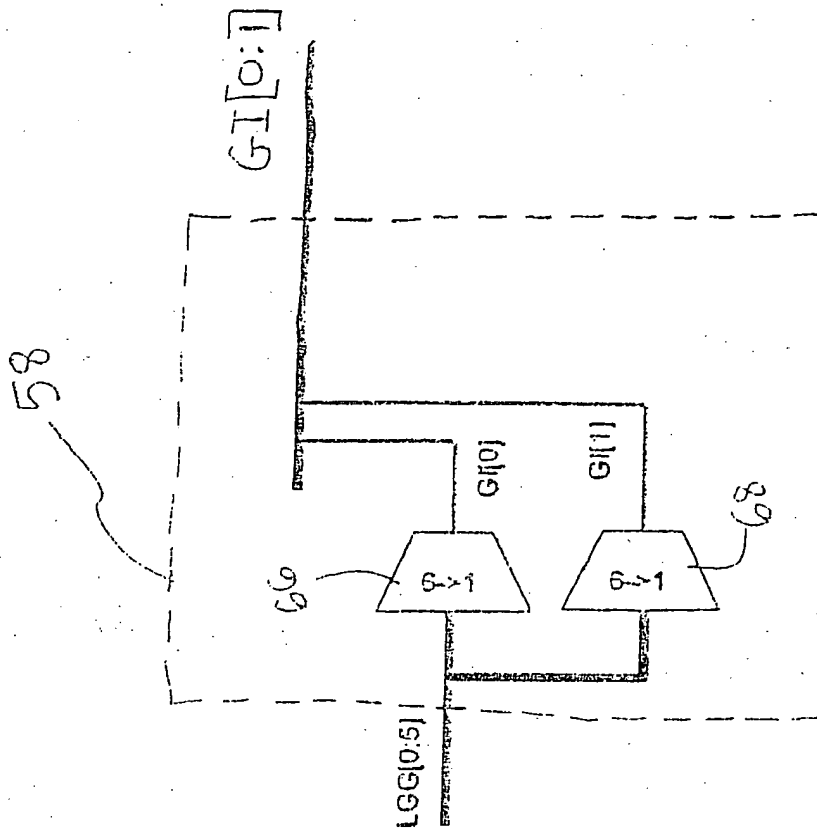


FIG. 8

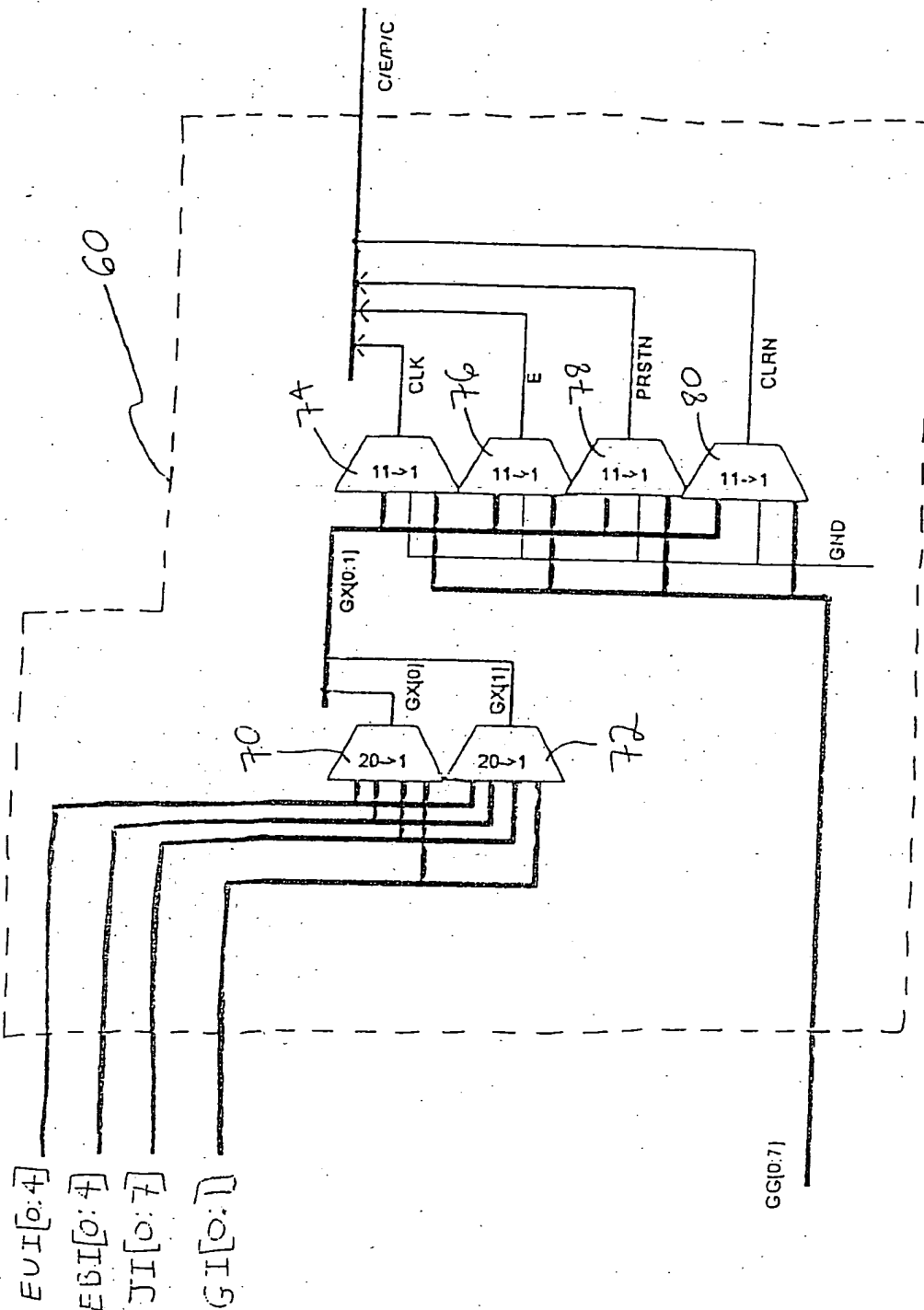


FIG. 9



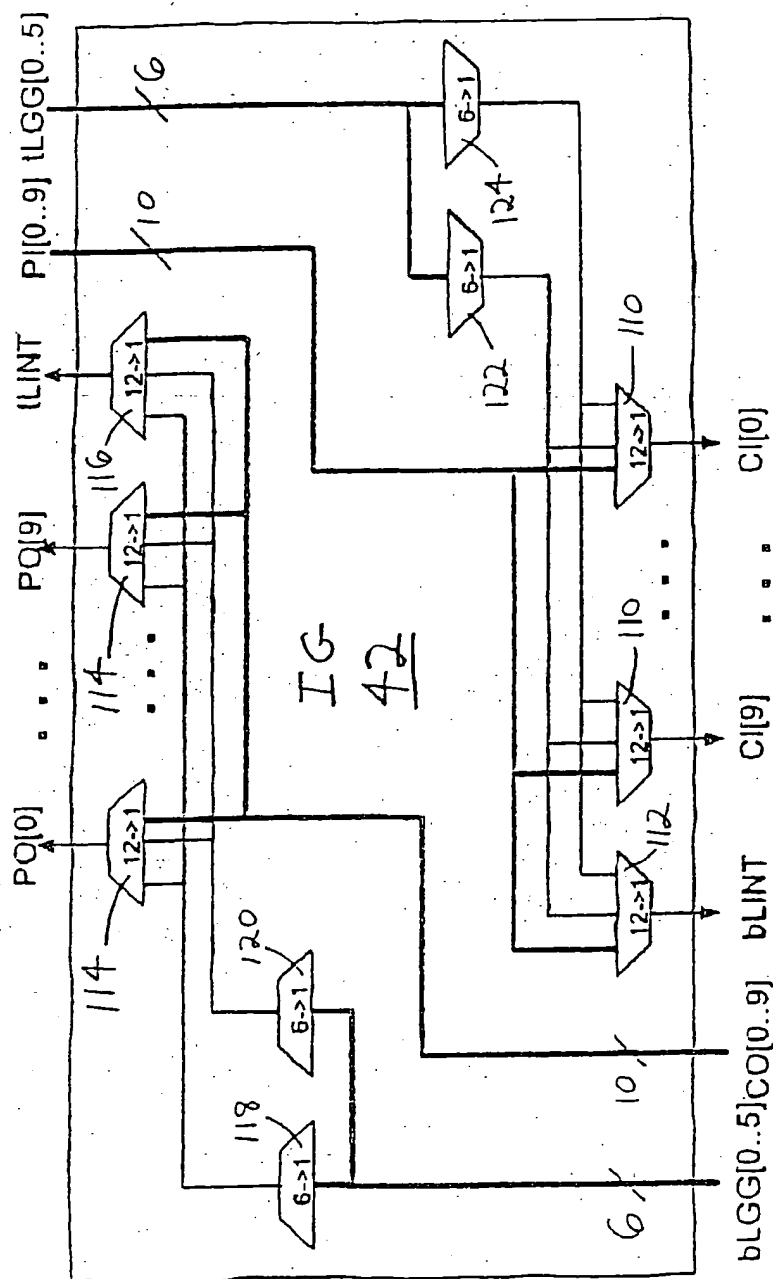


FIG. 11

205720" GET 4007

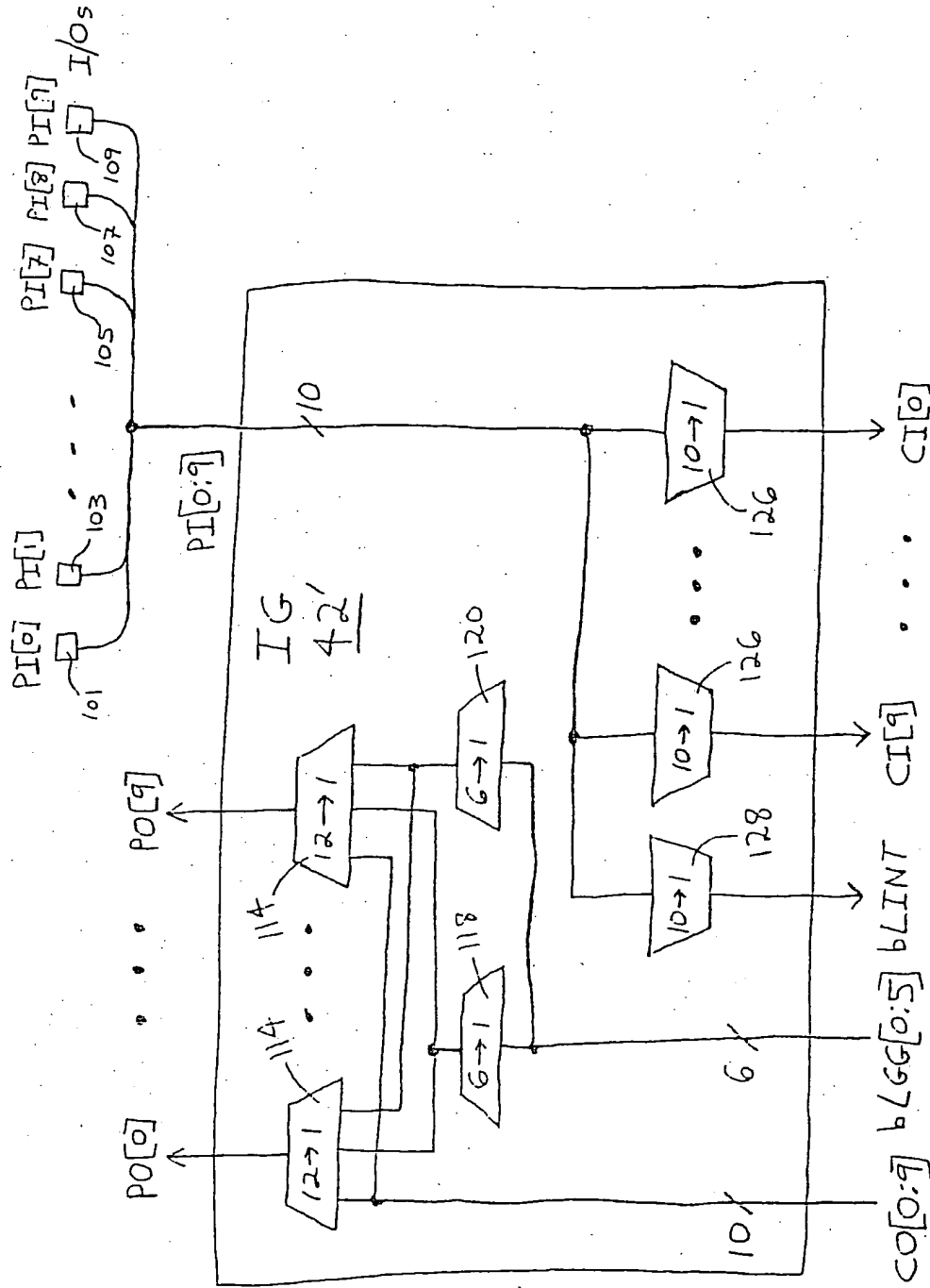
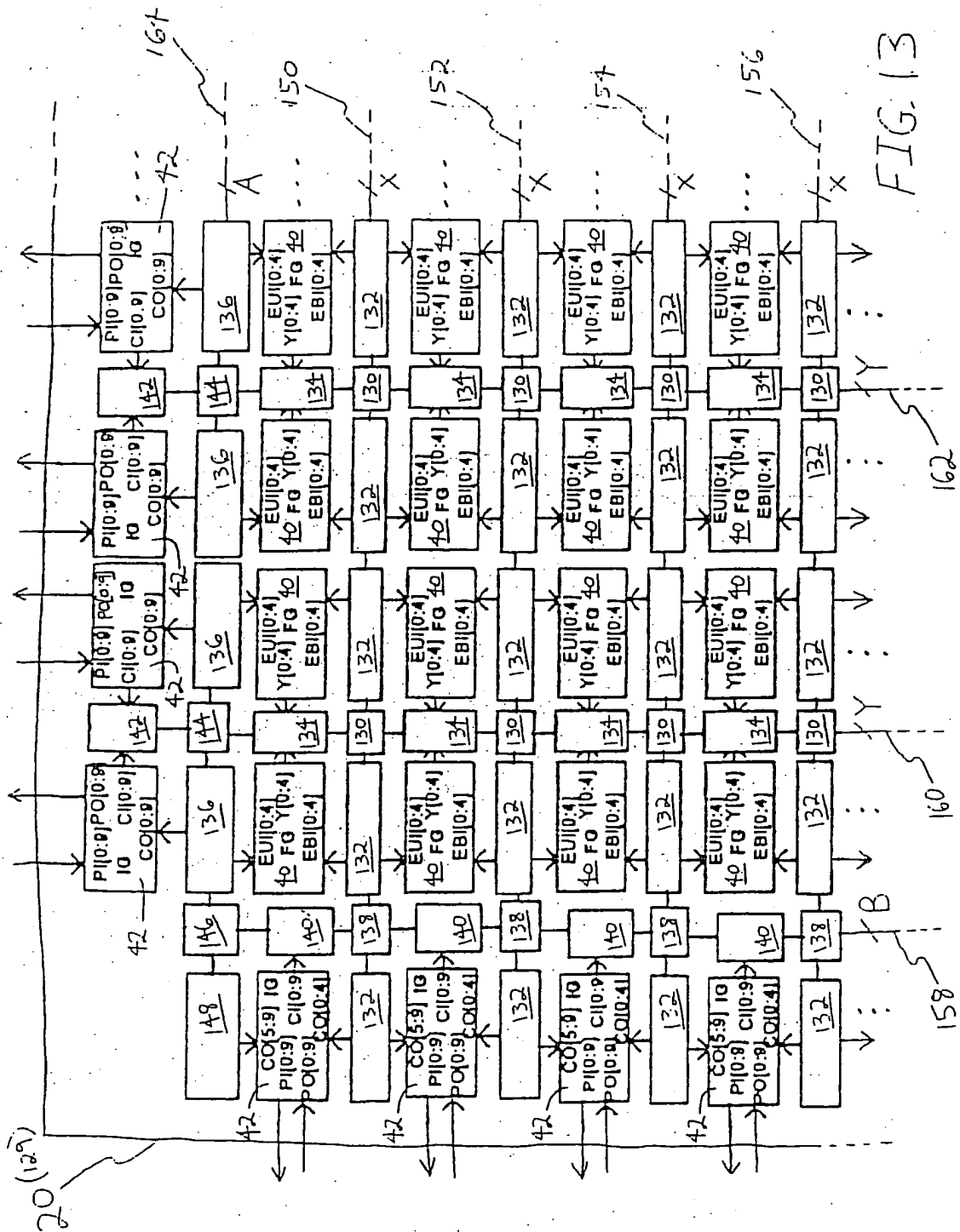
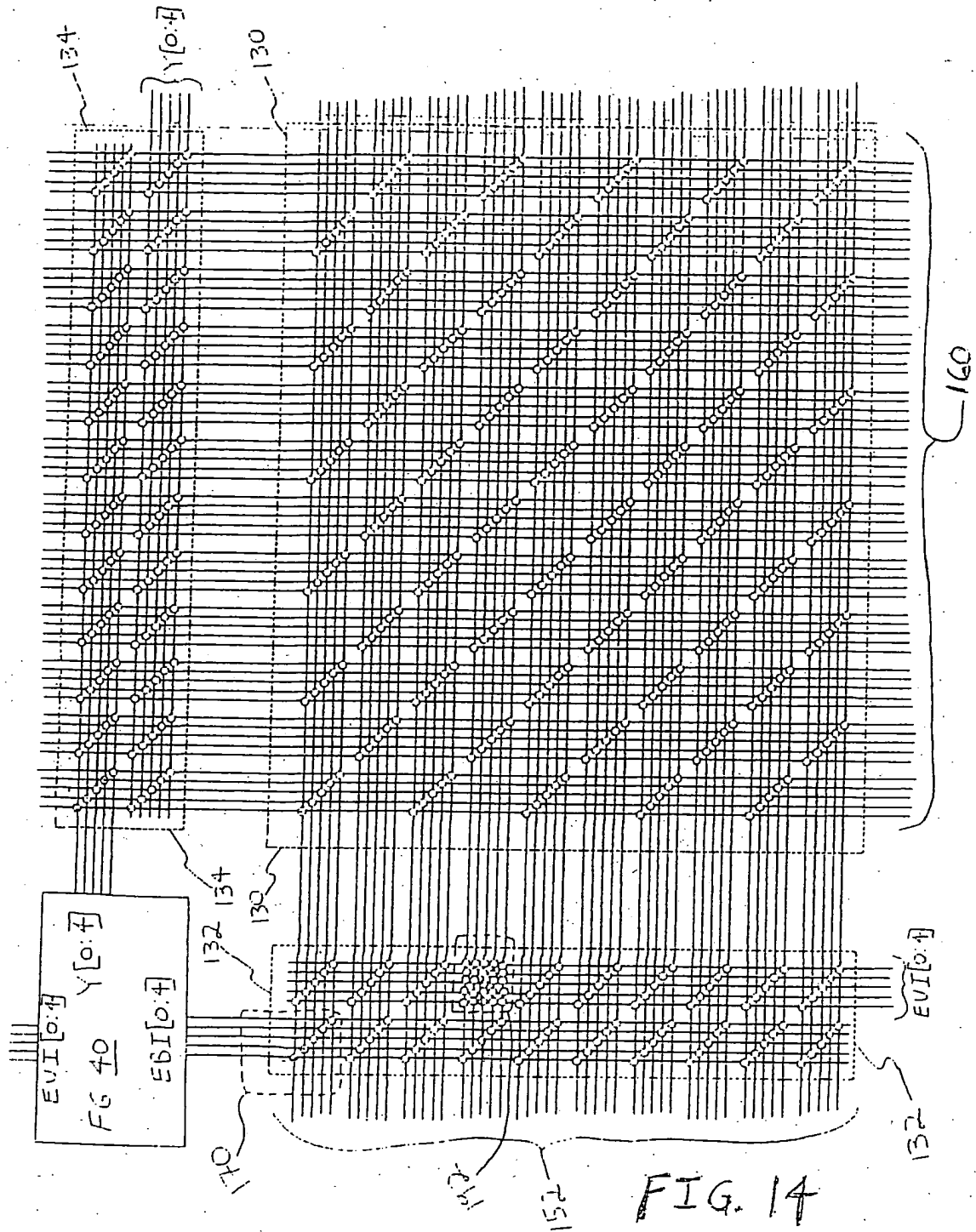


FIG. 12

205720" 5BT 2200T



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205T20" 68T400T

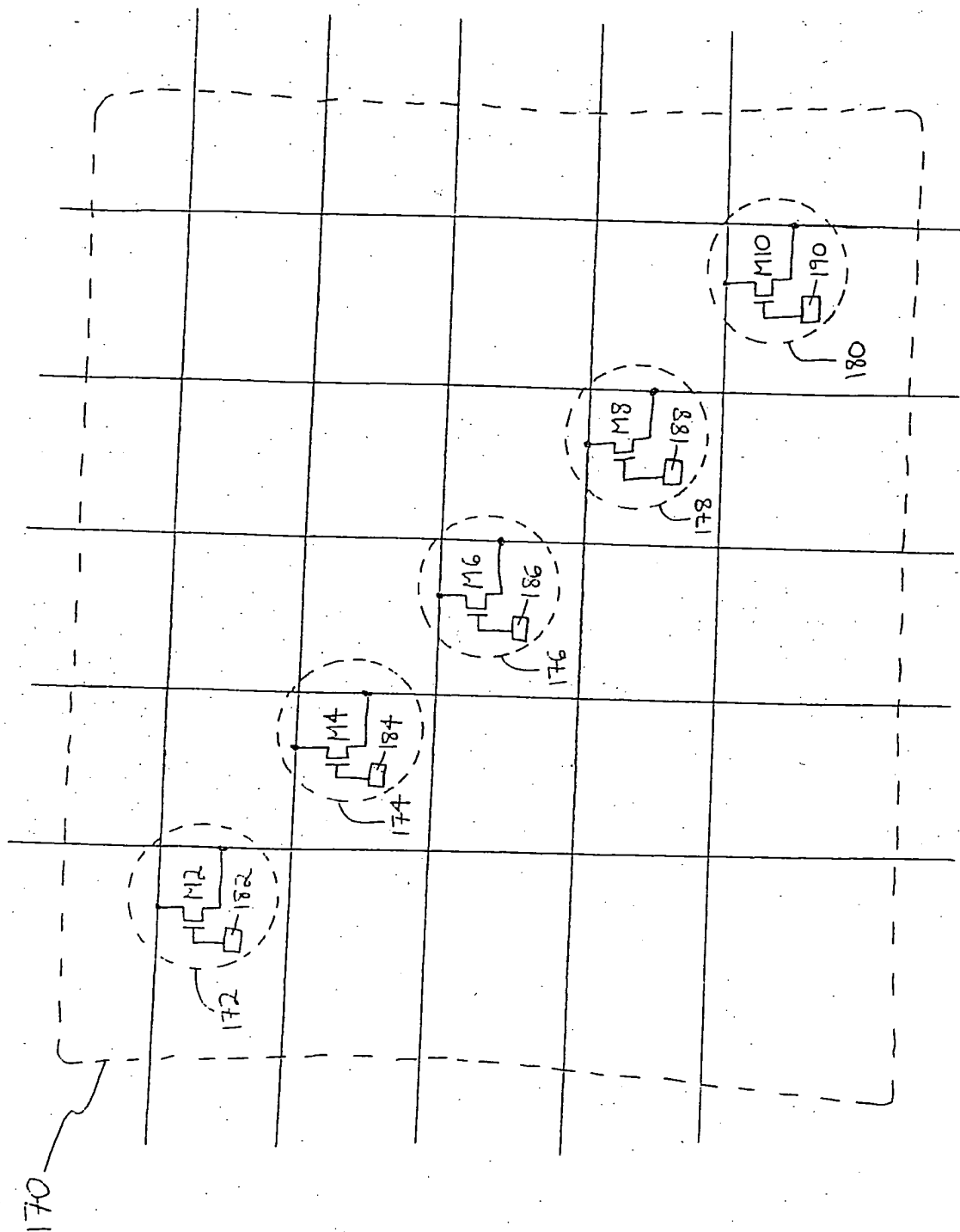


FIG. 15



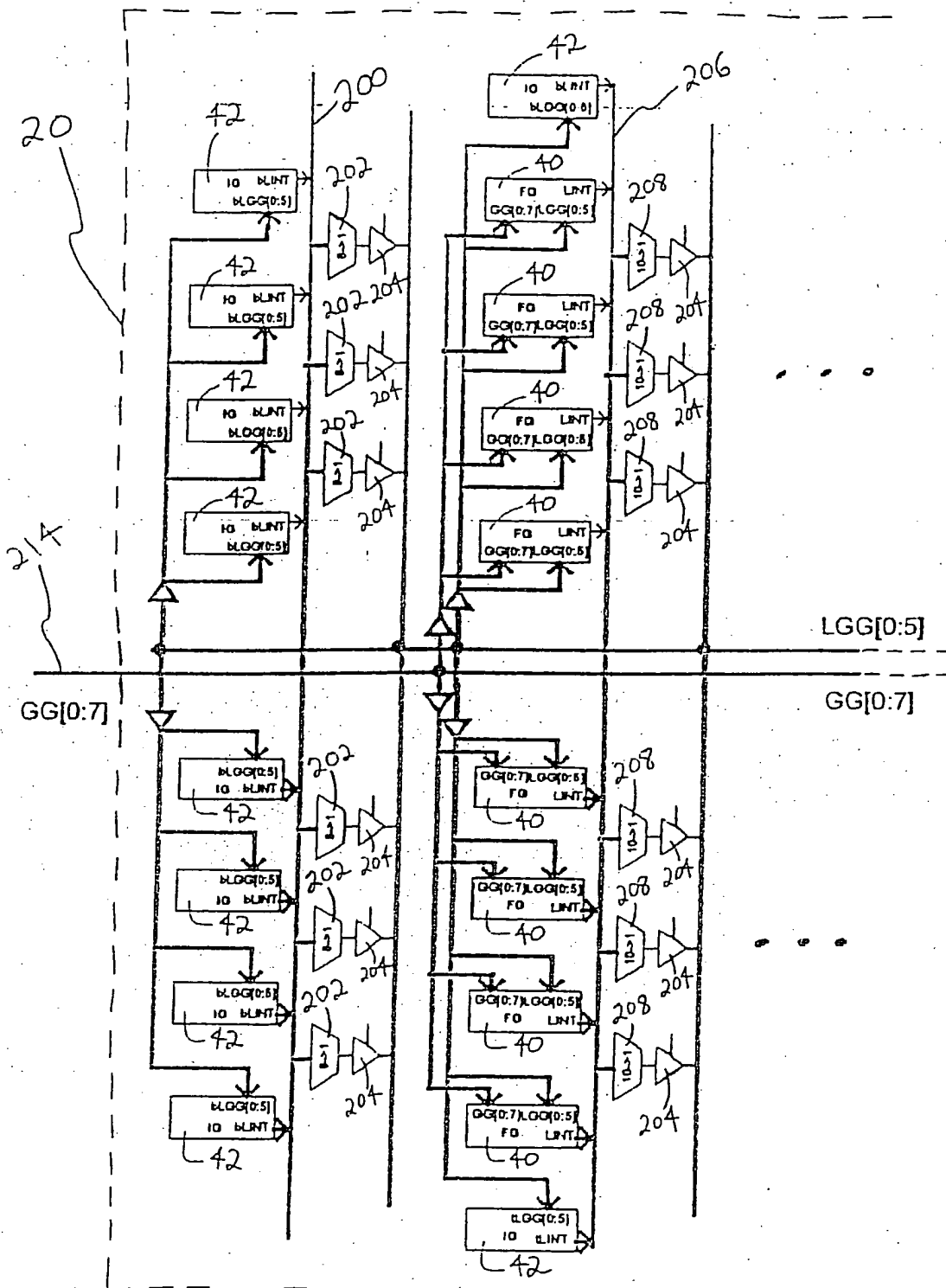


FIG. 16A

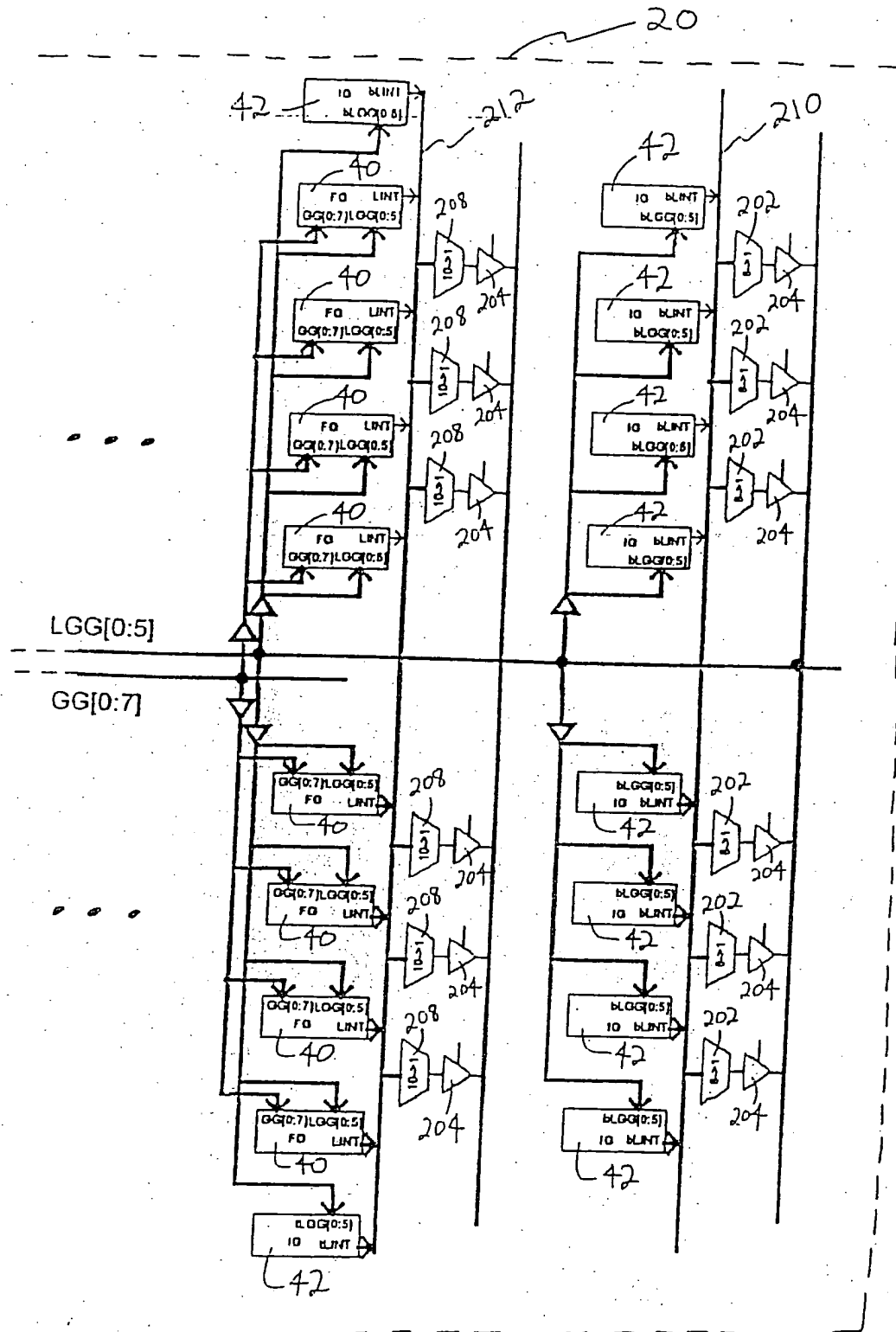


FIG. 16B

FIG. 17

205T20" 68T400T

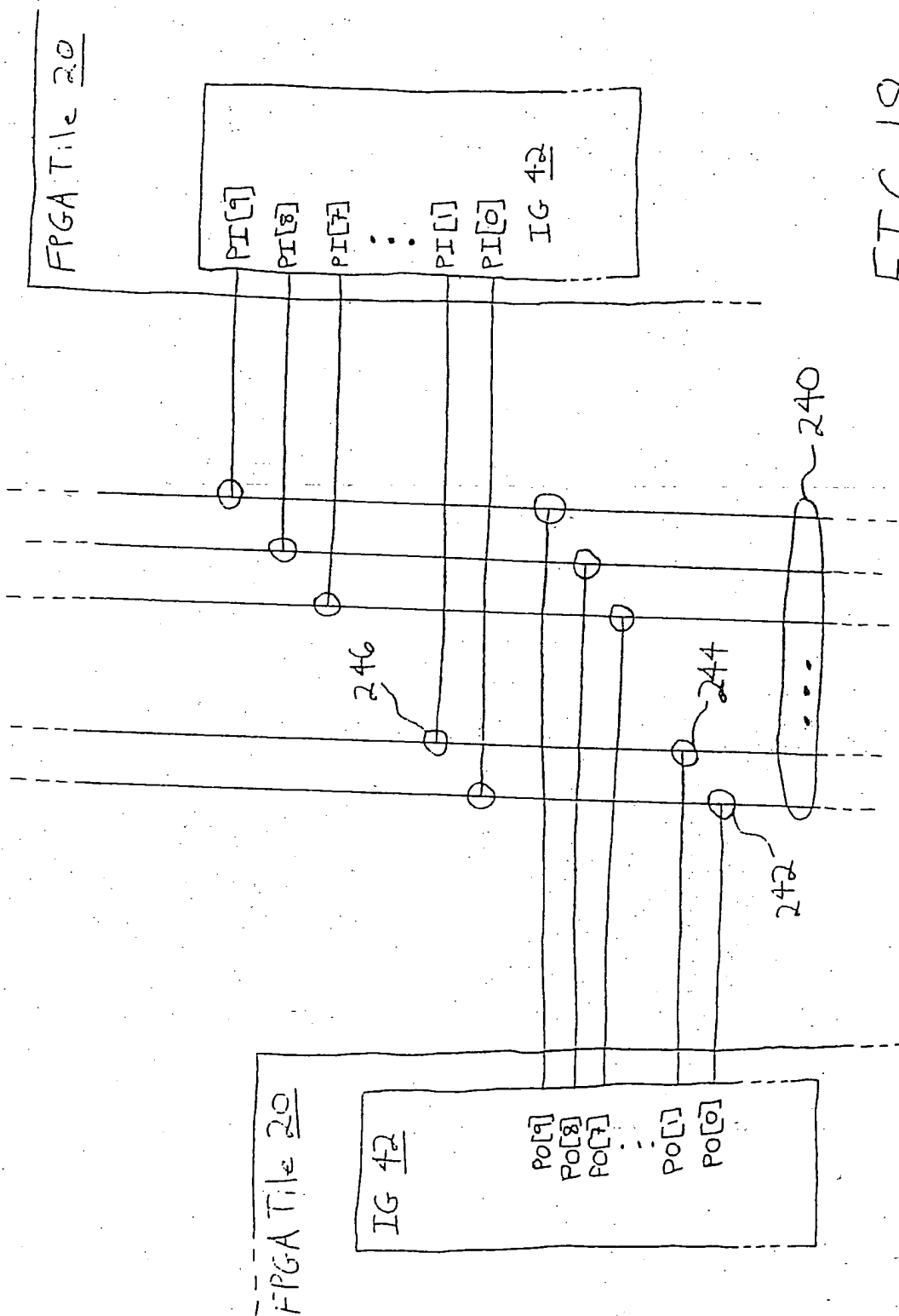


FIG. 19

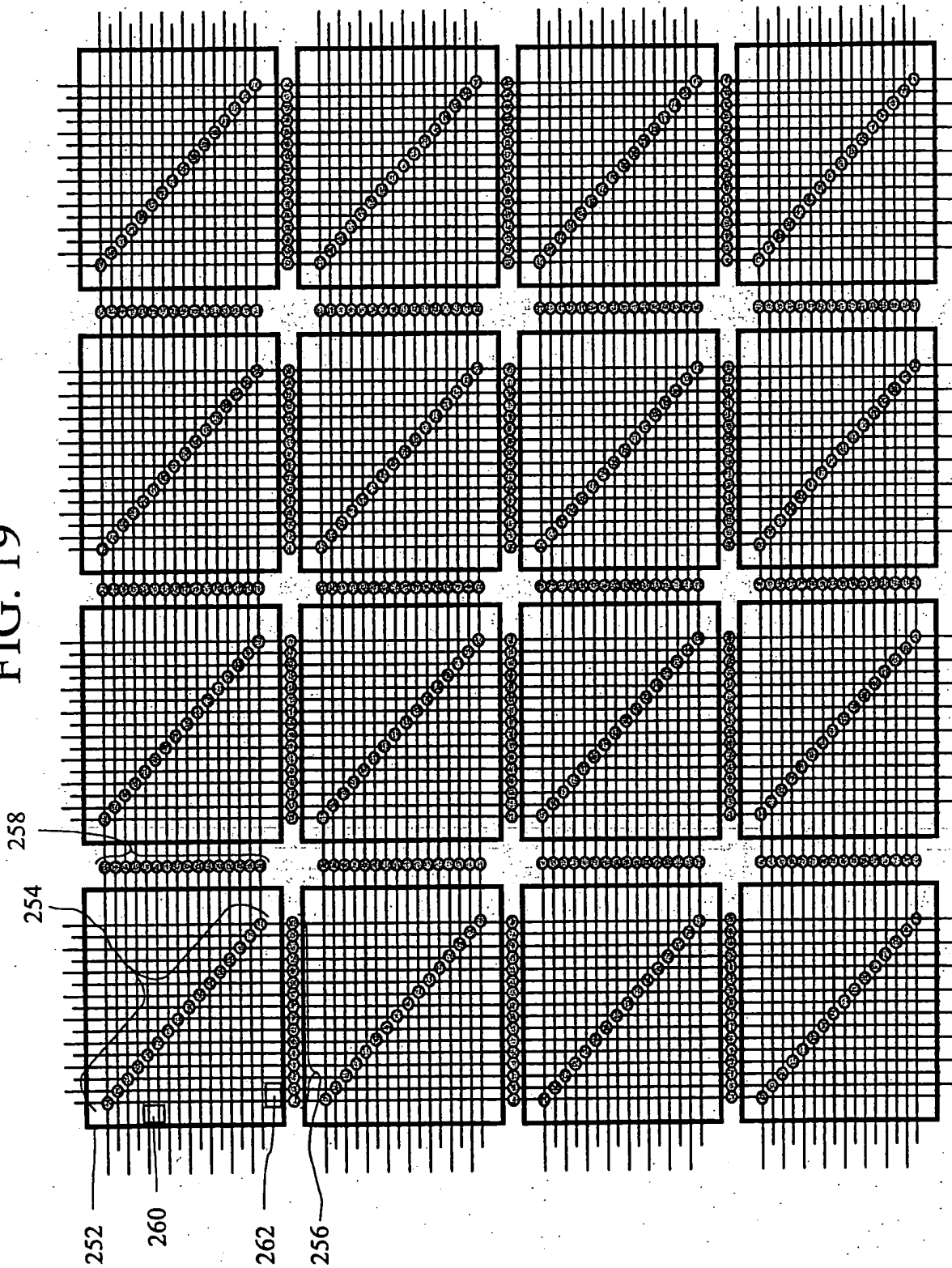
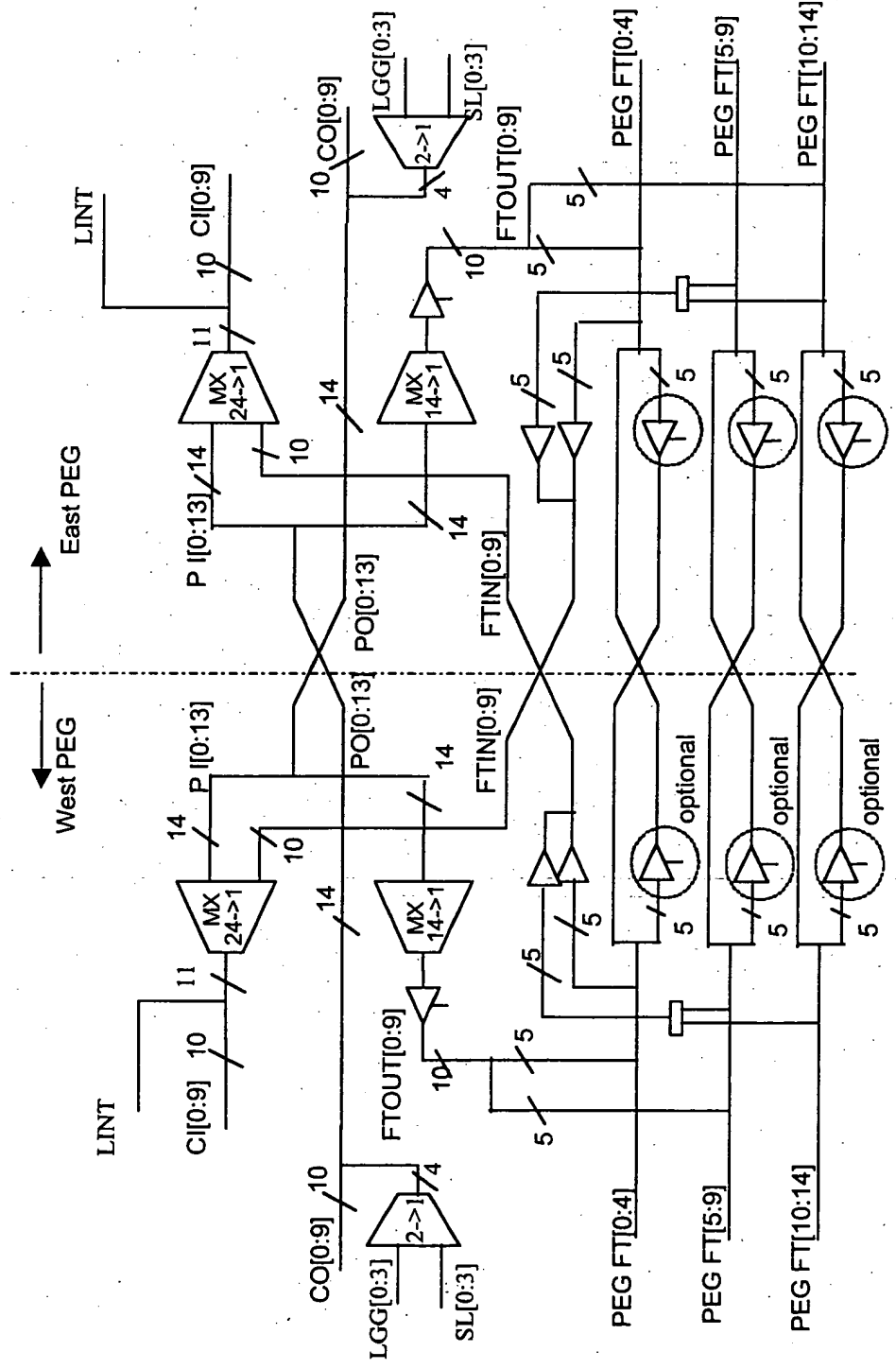


FIG. 20



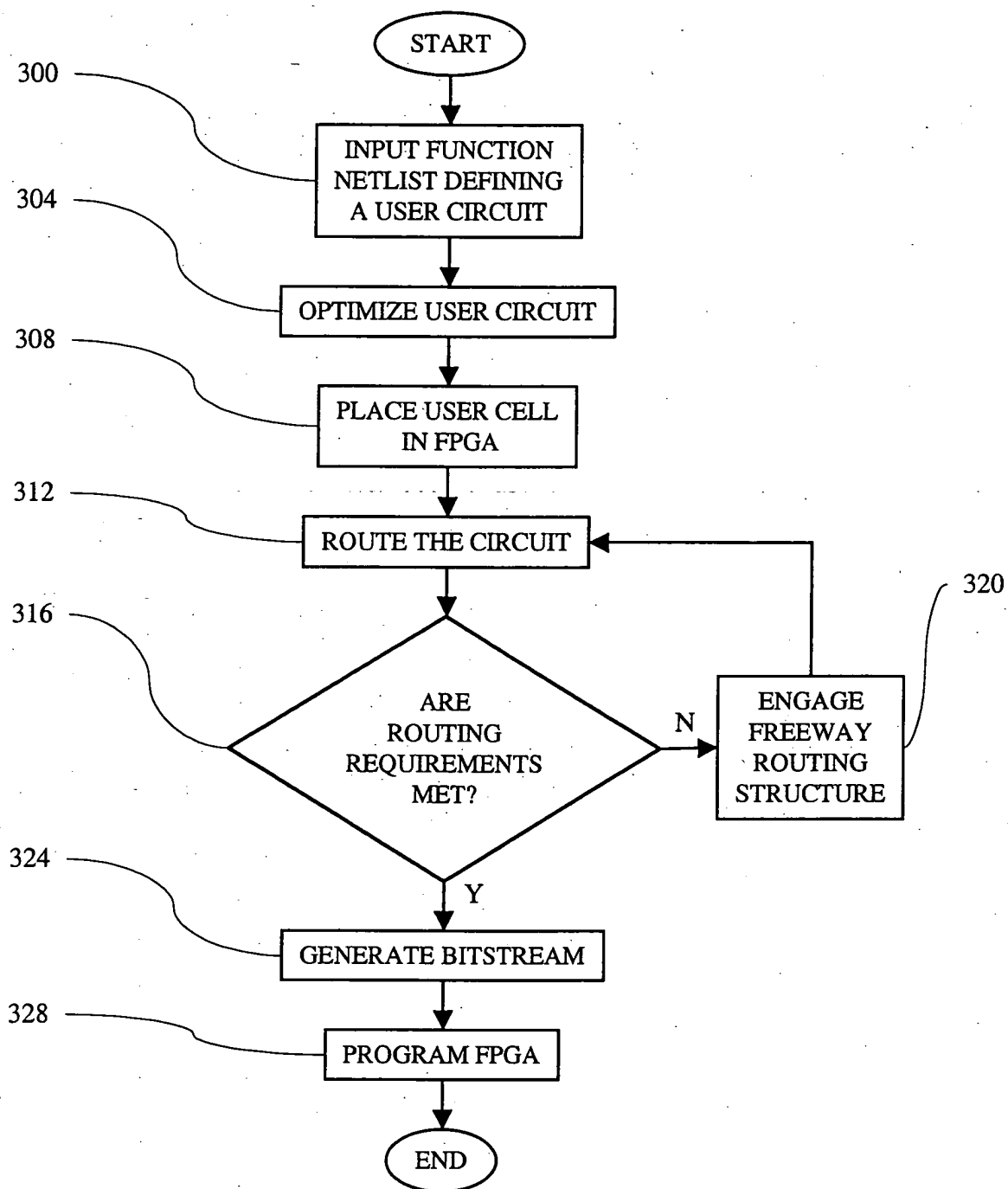


FIG. 21